

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	marcus.in. and janke	USPAT	OR	OFF	2007/06/22 18:46
L2	5	peter.in. and laackmann.in.	USPAT	OR	OFF	2007/06/22 19:02
L3	2018474	process or cpu	USPAT	OR	OFF	2007/06/22 18:15
L4	357	713/194.ccls.	USPAT	OR	OFF	2007/06/22 18:16
L5	0	l4 and high adj security adj mode	USPAT	OR	OFF	2007/06/22 18:16
L6	19	l3 and high adj security adj mode	USPAT	OR	OFF	2007/06/22 18:17
L7	4930	infineon adj technologies	USPAT	OR	OFF	2007/06/22 18:23
L8	580	l7 and processor	USPAT	OR	OFF	2007/06/22 18:34
L9	4	l8 and calculating adj units	USPAT	OR	OFF	2007/06/22 18:23
L10	218	l8 and calculat\$5	USPAT	OR	OFF	2007/06/22 18:45
L11	461265	chip or card or ic or integrated adj smart adj card or integrated adj card	USPAT	OR	OFF	2007/06/22 18:46
L12	47	l11 and dual adj rail adj logic	USPAT	OR	OFF	2007/06/22 18:46
L13	27	l12 and @ad<"20010701"	USPAT	OR	OFF	2007/06/22 18:49
L14	0	(calculat\$ and instruction and control\$4 and switch\$4on and security adj mode\$1).clm.	USPAT	OR	OFF	2007/06/22 18:51
L15	0	(processor and cryptograph\$4 and security adj mode\$1).clm.	USPAT	OR	OFF	2007/06/22 18:51
L16	5	(processor and cryptograph\$4 and security adj mode\$1).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 18:52
L17	1	(calculat\$ and instruction and control\$4 and switch\$4on and security adj mode\$1).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 18:51
L18	3107	processor with (plurality or multiple) near10 (calculat\$4 or comput\$4 or cryptograph\$4) near10 (unit or module\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 18:54

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L19	163	l18 and switch\$4 near3 (on or off)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 18:55
L20	2	l19 and (security adj mode\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 18:55
L21	121	726/34.ccls.	USPAT	OR	OFF	2007/06/22 19:03
L22	937	713/193.ccls.	USPAT	OR	OFF	2007/06/22 19:03
L23	1477	712/223-227.ccls.	USPAT	OR	OFF	2007/06/22 19:03
L24	2526	l21 or l22 or l23	USPAT	OR	OFF	2007/06/22 19:03
L25	70	l24 and processor with (multiple or plurality or (first and second)) with (comput\$4 or calculat\$4) near10 (unit\$1 or module\$1)	USPAT	OR	OFF	2007/06/22 19:11
L26	80	l24 and (processor or cpu) with (multiple or plurality or (first and second)) with (comput\$4 or calculat\$4) near10 (unit\$1 or module\$1)	USPAT	OR	OFF	2007/06/22 19:06
L27	1	l26 and switch\$4 near3 (on or off)	USPAT	OR	OFF	2007/06/22 19:08
L28	1	l26 and (on or off) near3 state	USPAT	OR	OFF	2007/06/22 19:13
L29	223	l24 and processor with (multiple or plurality or (first and second)) with (comput\$4 or calculat\$4 or register or execution) near10 (unit\$1 or module\$1)	USPAT	OR	OFF	2007/06/22 19:12
L30	1	l29 and (operat\$4 or execut\$4) with security near10 mode	USPAT	OR	OFF	2007/06/22 19:16
L31	0	high near10 security near10 mode with "on" near3 state	USPAT	OR	OFF	2007/06/22 19:20
L32	0	l29 and high near10 security near10 mode with "on" near3 state	USPAT	OR	OFF	2007/06/22 19:17
L33	1	l29 and parallel adj mode	USPAT	OR	OFF	2007/06/22 19:19
L34	0	l29 and parallel adj mode with "off" adj state	USPAT	OR	OFF	2007/06/22 19:19
L35	1	parallel adj mode with "off" adj state	USPAT	OR	OFF	2007/06/22 19:19

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L36	4224	processor with (plurality or multiple) near10 (calculating adj units or register\$1 or execution adj unit\$1 or cryptographic adj unit\$1)	USPAT	OR	OFF	2007/06/22 19:20
L37	0	l36 and (high adj security adj mode and parallel adj mode)	USPAT	OR	OFF	2007/06/22 19:21
L38	1	l36 and (security and parallel) with mode\$1	USPAT	OR	OFF	2007/06/22 19:38
L39	0	(chip or smart adj card or ic) and processor and security adj mode and parallel adj mode	USPAT	OR	OFF	2007/06/22 19:38
L40	2	(chip or smart adj card or ic) and processor and security adj mode and parallel adj mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 19:39
L41	113872	(chip or smart adj card or ic) and processor and (multiple or plurality) and (calculat\$4 or execution or crypto\$5 or register\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 19:40
L42	113876	(chip or smart adj card or ic) and (processor or cput) and (multiple or plurality) and (calculat\$4 or execution or crypto\$5 or register\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/22 19:40


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Terms used: chip or smart card or ic and processor

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1 [FACADE: a typed intermediate language dedicated to smart cards](#)

Gilles Grimaud, Jean-Louis Lanet, Jean-Jacques Vandewalle

 October 1999 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 7th European software engineering conference held jointly with the 7th ACM SIGSOFT international symposium on Foundations of software engineering ESEC/FSE-7**, Volume 24 Issue 6

Publisher: Springer-Verlag, ACM.Press

Full text available: [pdf\(1.23 MB\)](#)
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The use of smart cards to run software modules on demand has become a major business concern for application issuers. Such down-loadable executable content needs to be trusted by the card execution environment in order to ensure that an instruction on a memory area is compliant with the definition of the data stored in this area (i.e. its type). Current solutions for smart cards rely on three techniques. For Java Card, either an off-card verifier-converter performs a static ...

2 [Verifiable partial key escrow](#)

Mihir Bellare, Shafi Goldwasser

 April 1997 **Proceedings of the 4th ACM conference on Computer and communications security CCS '97**

Publisher: ACM Press

Full text available: [pdf\(1.98 MB\)](#)
 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Authentication in distributed systems: theory and practice](#)

Butler Lampson, Martín Abadi, Michael Burrows, Edward Wobber

 November 1992 **ACM Transactions on Computer Systems (TOCS)**, Volume 10 Issue 4

Publisher: ACM Press

Full text available: [pdf\(3.37 MB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

We describe a theory of authentication and a system that implements it. Our theory is based on the notion of principal and a "speaks for" relation between principals. A simple principal either has a name or is a communication channel; a compound principal can express an adopted role or delegated authority. The theory shows how to reason about a principal's authority by deducing the other principals that it can speak for; authenticating

a channel is one important application. We ...

Keywords: certification authority, delegation, group, interprocess communication, key distribution, loading programs, path name, principal, role, secure channel, speaks for, trusted computing base

4 Designing computer systems with MEMS-based storage



Steven W. Schlosser, John Linwood Griffin, David F. Nagle, Gregory R. Ganger

November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5

Publisher: ACM Press

Full text available: [pdf\(439.06 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

For decades the RAM-to-disk memory hierarchy gap has plagued computer architects. An exciting new storage technology based on microelectromechanical systems (MEMS) is poised to fill a large portion of this performance gap, significantly reduce system power consumption, and enable many new applications. This paper explores the system-level implications of integrating MEMS-based storage into the memory hierarchy. Results show that standalone MEMS-based storage reduces I/O stall times by 4-74X over ...

5 Designing computer systems with MEMS-based storage



Steven W. Schlosser, John Linwood Griffin, David F. Nagle, Gregory R. Ganger

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Publisher: ACM Press

Full text available: [pdf\(439.06 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

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6 Mobile computing: beyond laptops



Laura Cappelletti

October 1997 **Proceedings of the 15th annual international conference on Computer documentation SIGDOC '97**

Publisher: ACM Press

Full text available: [pdf\(406.32 KB\)](#) Additional Information: [full citation](#), [index terms](#)

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Steven W. Schlosser, John Linwood Griffin, David F. Nagle, Gregory R. Ganger

November 2000 **ACM SIGOPS Operating Systems Review**, **ACM SIGARCH Computer Architecture News**, **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 34, 28 Issue 5, 5

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